

### **Amendments to the Specification**

Please replace Paragraph [016] with the following amendment paragraph:

[016] Referring to Figure 1, interface 10 further comprises a choke 30, through which signal lines 16, 18, 20, and 22 pass for purposes of improving EMC immunity. Choke 30 preferably comprises a ferrite core with inductors positioned therein that permit the lines 16-22 to pass therethrough while neglecting shield and permitting fault currents to pass by on the shield uninhibited while blocking common mode currents with ferrite impedance much stronger than could be tolerated on each data line individually. Power source 14 contains bypass capacitors 32, such as Tantalum bypass capacitors, and a 5.6V transorb device 34. All unused pins ~~28~~ 38 are terminated to reduce ringing transients in the controller lead frame.